

FIG. 1

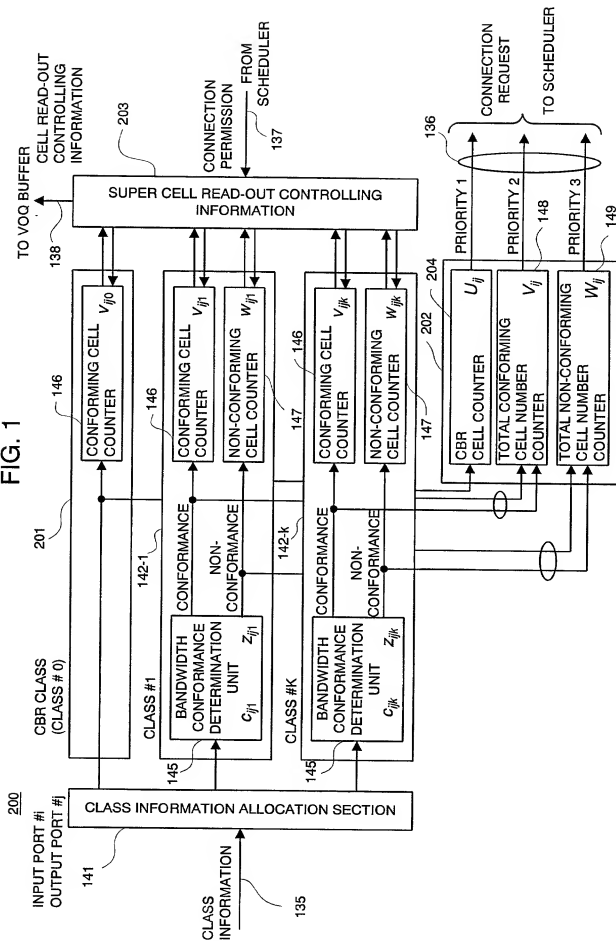


FIG. 2

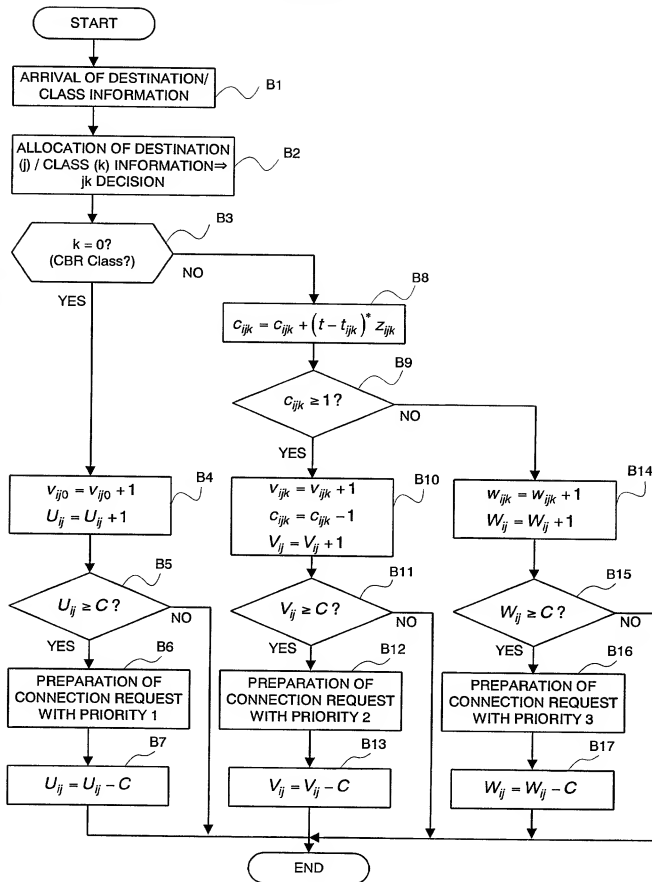


FIG. 3

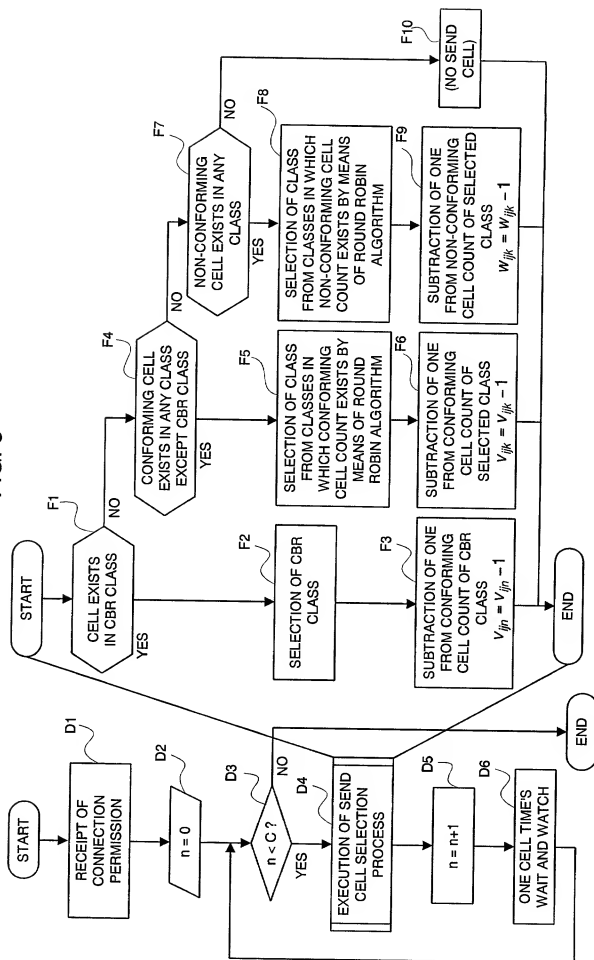


FIG. 4

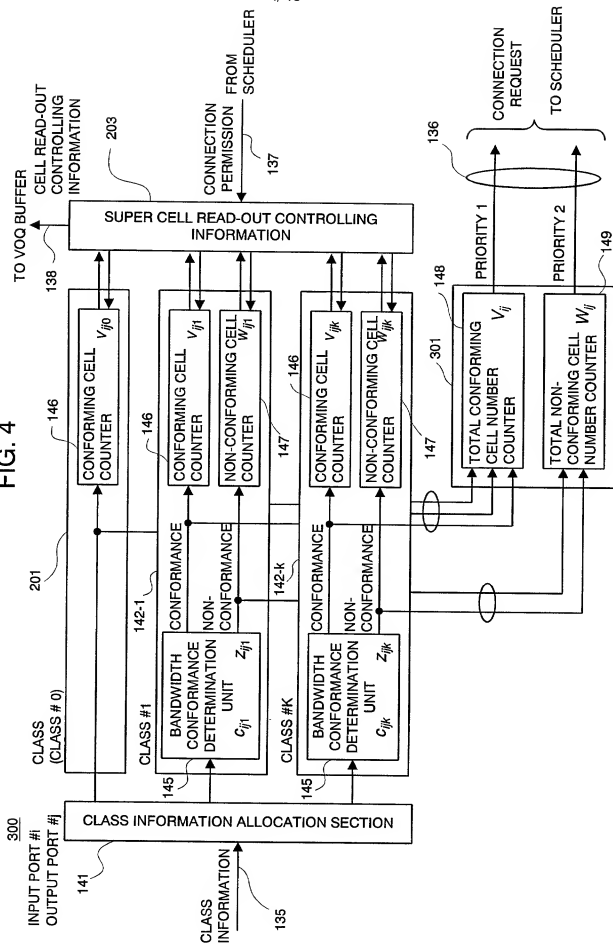
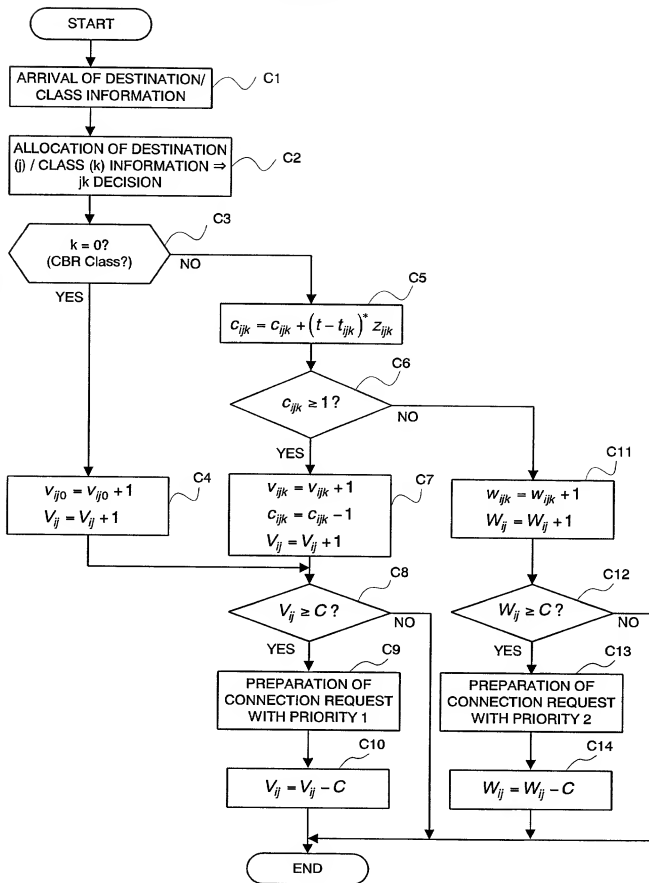


FIG. 5



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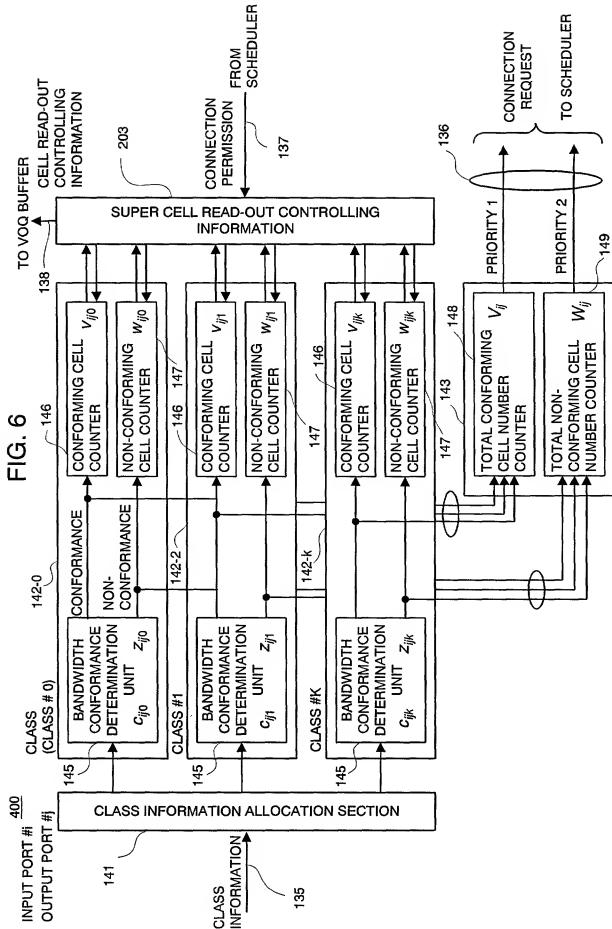


FIG. 7

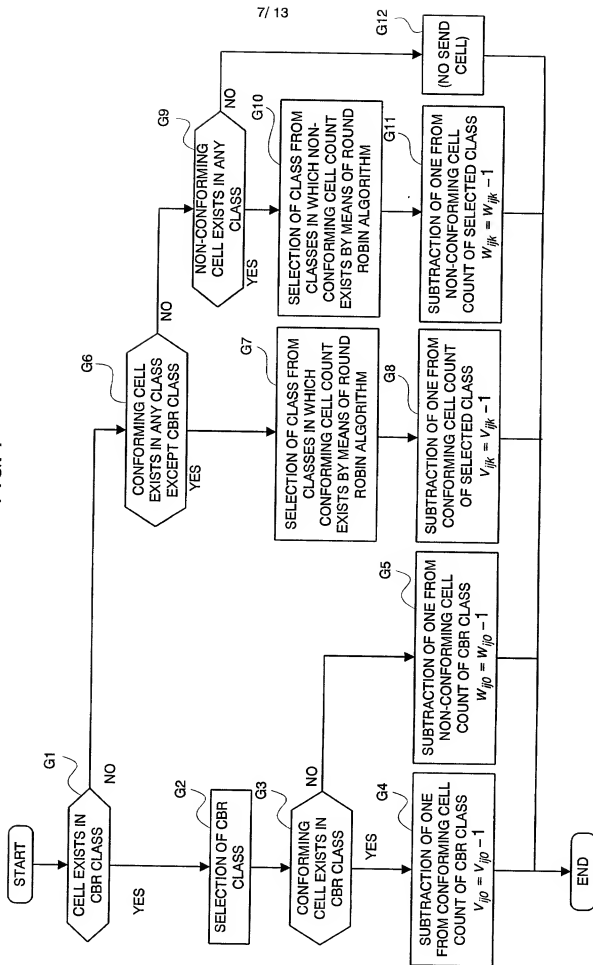


FIG. 8

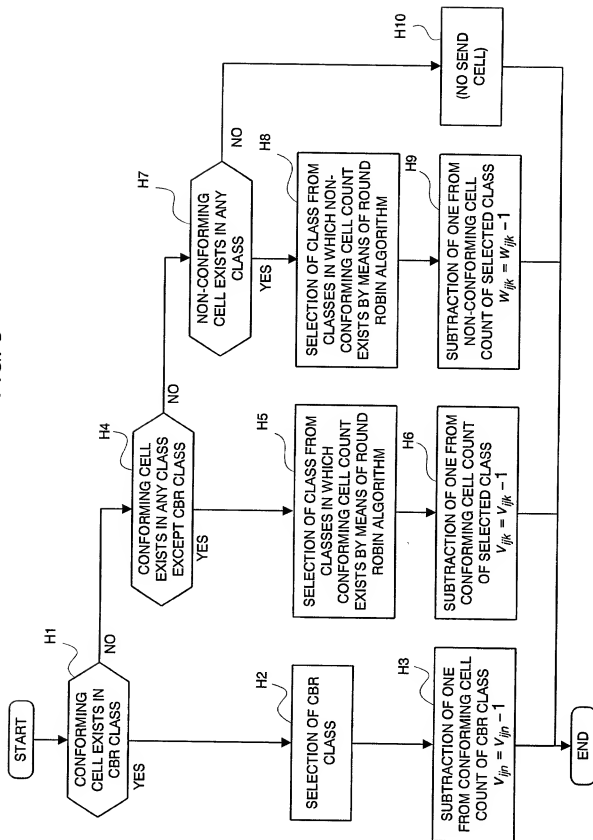




FIG. 9  
INPUT BUFFERING  
PACKET SWITCH

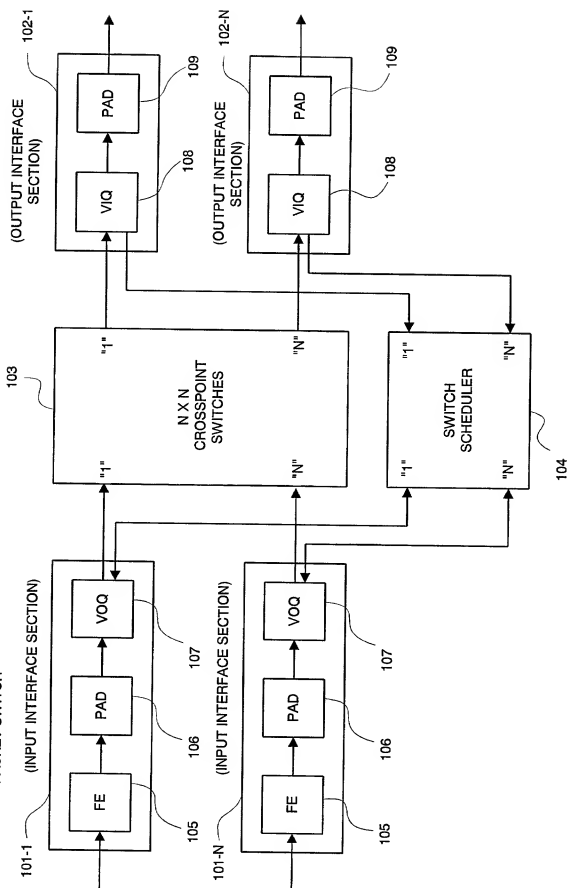


FIG. 10

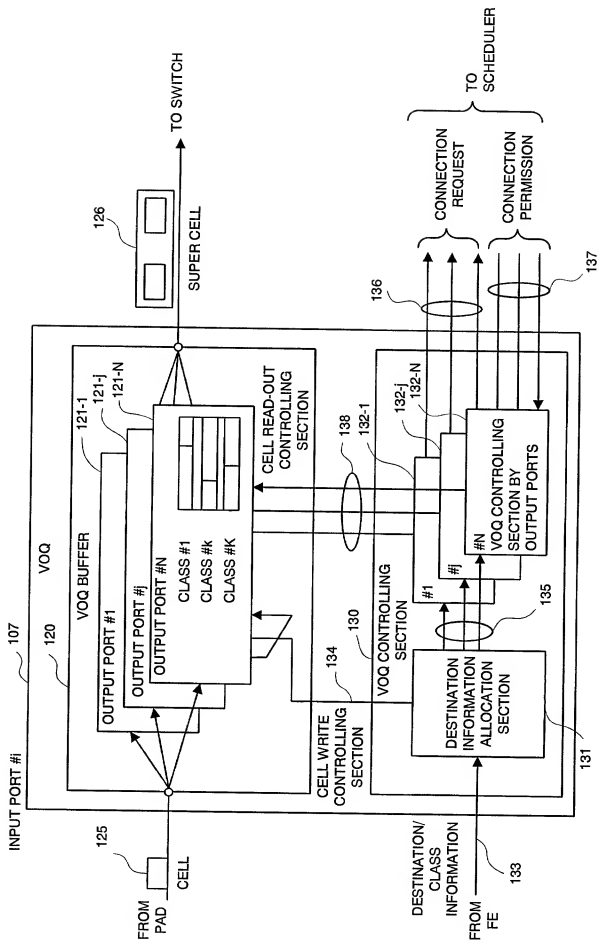


FIG. 11  
PRIOR ART

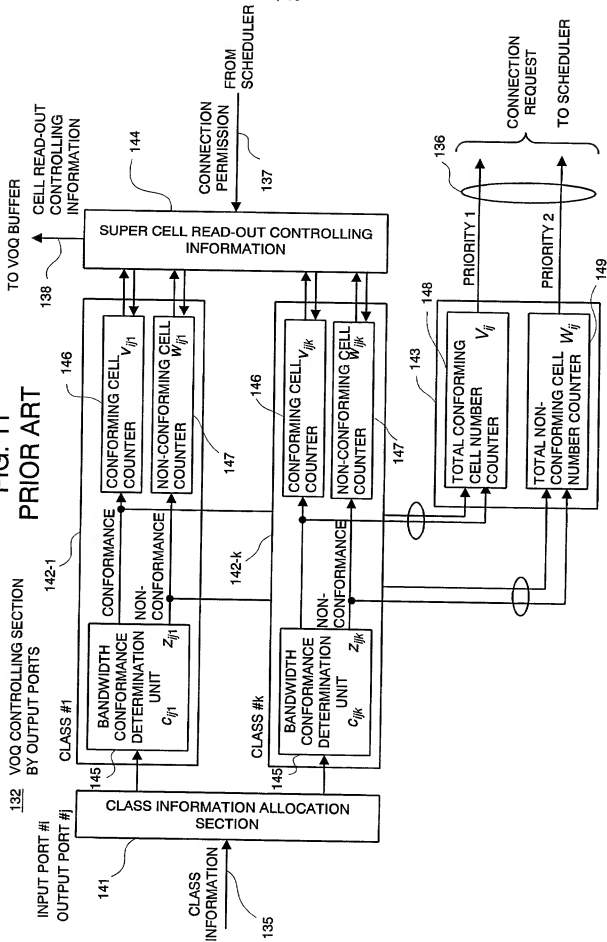


FIG. 12  
PRIOR ART

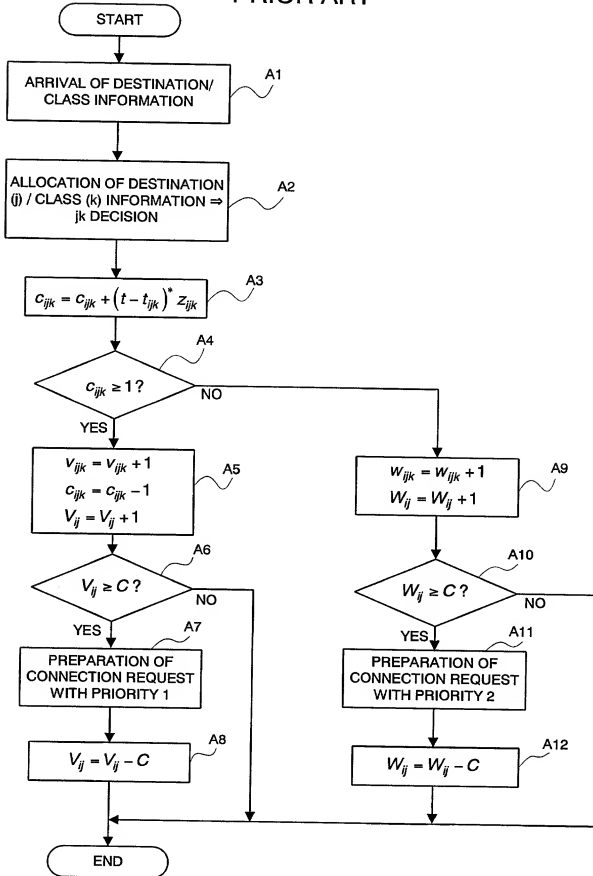


FIG. 13  
PRIOR ART